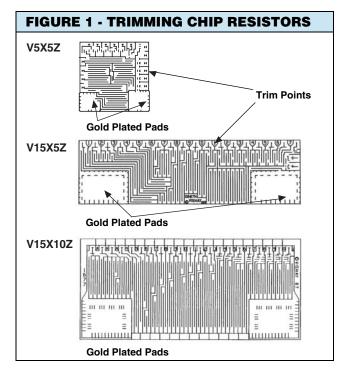


Ultra High Precision Bulk Metal[®] Z-Foil Chip Resistors for Use in Hybrid Circuits with TCR of <u>0.05 ppm/°C</u>, Tolerance to <u>0.005 %</u>, and Load Life Stability of ± 0.01 % for <u>10 000 h</u>



VISHAY PRECISION

GROUP

The typical pattern and trimming illustrations show that the V5X5Z resistor has 16 trimming points, the V15X5Z has 20 and the V15X10Z has 21. These trimming points are arranged around the chip periphery and are clearly indicated. Trimming to the desired resistance value and tolerance is accomplished by cutting the trim points, thereby producing specific incremental changes in the chip's resistance value relative to the original prevalue; up to + 20 % for the V5X5Z, + 30 % for the V15X5Z, and + 50 % for the V15X10Z (not all trim points need be used; the ΔR necessary to adjust the pre-value to the desired final value dictates which trim points need to be used).

Monitoring of circuit output while "actively" trimming readily permits adjustment of the chip to \pm 0.005 %.

Actual trimming charts are supplied on request for all images.

Vishay precision chip resistors offer an order of magnitude of improvement over other chip resistors in hybrid applications. With a maximum Temperature Coefficient of Resistance (TCR) of ± 2 ppm/°C, selected TCR tracking to 0.5 ppm/°C and factory supplied resistance tolerances to \pm 0.01 %, they provide the user with accuracy and stability not available in other chip resistor products. If desired they can be user trimmed to any value within \pm 0.005 %, where the value remains stable after trimming. Load life stability is 0.05 % ΔR maximum under full rated power for 2000 h at

FEATURES

 Temperature coefficient of resistance (TCR): 0.05 ppm/°C typical (0 °C to + 60 °C) 0.2 ppm/°C typical (- 55 °C to + 125 °C, + 25 °C ref.) TCR tracking: to 0.5 ppm/°C ⁽¹⁾⁽²⁾



- ROHS COMPLIANT
- TCR characteristic is accomplished automatically without selection and regardless of the date of manufacture even if years apart
- Hybrid chips with Z-Foil are also available for high temperature applications, please contact us for more details
- Resistance tolerance: Absolute: to \pm 0.01 % (user trimmable to \pm 0.005 %) Match: to 0.01 %
- Power rating: 50 mW to 150 mW at + 70 °C
- Load life stability: \pm 0.01 % at + 70 °C, 10 000 h at rated power
- Resistance range: 5 Ω to 60 k Ω (see table 2)
- Vishay Foil resistors are not restricted to standard values; specific "as required" values can be supplied at no extra cost or delivery (e.g. 1K2345 vs. 1K)
- Short time overload: ≤ 0.02 %
- Electrostatic discharge (ESD) up to 25 000 V
- Non-inductive, non-capacitive design
- Rise time: 1 ns effectively no ringing
- Thermal stabilization time < 1 s (nominal value achieved within 10 ppm of steady state value)
- Current noise: 0.010 μ V_{RMS}/V of applied voltage (< 40 dB)
- Non-inductive: < 0.08 μH
- Pattern design minimizing hot spots

+ 70 °C. Shelf life stability is 0.0005 % ΔR maximum per year (hermetically sealed).

The Vishay precision trimming system allows for adjustment to precise resistance values without concern over mechanical override and control problems encountered in laser or air abrade trimming of solid geometry resistance patterns. This ability to trim resistor chips to tolerance levels never before available to hybrid manufacturers, now gives a project manager the ability to increase the value-added level of their hybrid services. More of the profit thus available can be retained within the facility. Now, instead of buying precision resistors in separate packages or modules (which require additional PC board real estate) and integrating them into a system, the project manager can utilize Vishay precision resistor chips or matched sets to manufacture the entire hybrid circuit in-house. Eliminates the need to "pin-out" for precision resistor requirements because the precision resistors are inside - part of the hybrid microcircuit design.

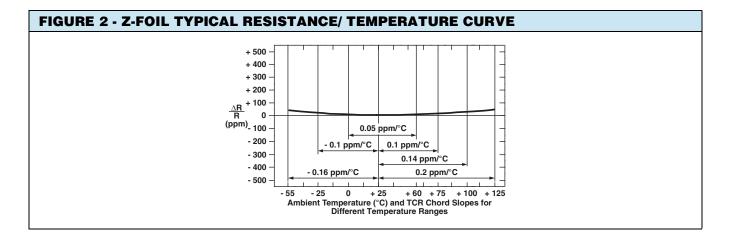
Vishay precision chip resistors are available either factorytrimmed to exact resistance values (option T) or ready for user trimming (option U); user trimming can be done either before or after bonding - using standard epoxies - onto the hybrid circuit substrate using standard laser, air abrade, or manual adjustment techniques. However care should be taken that no carbonization occurs when laser trimming.

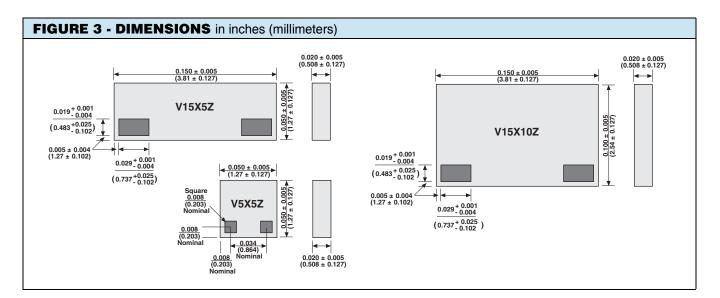


| TABLE 1 - TOLERANCE AND TCR VS. RESISTANCE VALUE | | | |
|--|---------------------------|--|--|
| VALUE (Ω) | STANDARD TOLERANCE (%) | TYPICAL TCR AND MAX SPREAD - 55 °C to + 125 °C, + 25 °C Ref. (ppm/°C) | |
| 500 to 60K | ± 0.01 | ± 0.2 ± 1.8 | |
| 100 to < 500 | ± 0.02 | ± 0.2 ± 1.8 | |
| 50 to < 100 | ± 0.02 | ± 0.2 ± 2.8 | |
| 20 to < 50 | ± 0.05 | ± 0.2 ± 3.8 | |
| 10 to < 20 | ± 0.05 | ± 0.2 ± 3.8 | |
| 5 to < 10 | ± 0.1 | ± 0.2 ± 4.8 | |

Note

• For tighter performances or high temperature applications, please contact Vishay application engineering







| TABLE 2 - PRECISION CHIP RESISTOR SPECIFICATIONS | | | |
|---|--|--|--|
| Resistance range | 5 Ω to 5 kΩ (model V5X5Z) 5 Ω to 25 kΩ (model V15X5Z) 25 kΩ to 60 kΩ (model V15X10Z) | | |
| TCR (- 55 °C to + 125 °C, + 25 °C Ref. (ppm/°C) | See Table 1 | | |
| Trimming range (Approximate adjustment capability) | 0 to 1.2 x nominal prevalue (V5X5Z)* 0 to 1.3 x nominal prevalue (V15X5Z) 0 to 1.5 x nominal prevalue (V15X10Z) Note * The V5X5Z chips are being gradually redesigned for a higher trimming factor - 1.3 or more instead of 1.2. For information about the availability of a specific resistance value, contact the factory. | | |
| Resistance tolerance | $\begin{array}{l} \mbox{Option T (trimmed to value at Vishay)}^* \\ \pm 0.01 \ \%; \pm 0.02 \ \%; \pm 0.05 \ \%; \pm 0.1 \ \%; \pm 0.25 \ \%; \pm 0.5 \ \%; \pm 1 \ \%; \pm 5 \ \% \\ \mbox{Option U (for user trimming to any value within \pm 0.005 \ \%)} \\ \mbox{G.F.} = Good for values \\ \mbox{Note} \\ ^* \ See \ table \ 1 \ for \ resistance/tolerance limits \\ \end{array}$ | | |
| Power rating (at + 70 °C ambient temperature), (see figure 4) | V5X5Z: 0.05 W (15 V maximum) V15X5Z: 0.1 W (50 V maximum) V15X10Z: 0.15 W (95 V maximum) | | |
| High frequency operation Rise time Inductance Capacitance | 1 ns without ringing 0.1 μH maximum; 0.08 μH typical 1.0 pF maximum; 0.5 pF typical | | |
| Current noise | < 0.010 µV _{RMS} /V of applied voltage (< - 40 dB) | | |
| Voltage coefficient | < 0.1 ppm/V | | |
| Working voltage | 15 V (model V5X5Z) 50 V (model V15X5Z) 95 V (model V15X10Z) | | |
| Termination pads | Gold: 50 μ " to 100 μ " thick | | |

| TABLE 3 - ENVIRONMENTAL PERFORMANCE COMPARISON | | | |
|---|---|--|--|
| | TYPICAL VISHAY | MAXIMUM | |
| Test group I Thermal shock | ± 0.02 % | ± 0.04 % | |
| Test group II Low temperature operation Short time overload High temperature exposure Resistance to bonding exposure | $\begin{array}{c} \pm \ 0.005 \ \% \\ \pm \ 0.02 \ \% \end{array}$ | ± 0.01 % ± 0.04 % ± 0.05 % ± 0.05 % | |
| Test group III Moisture resistance | ± 0.03 % | ± 0.1 % | |
| Test group IV (Load Life Stability) 2000 h at + 70 °C 10 000 h at + 70 °C | ± 0.005 % ± 0.01 % | ± 0.015 % ± 0.05 % | |

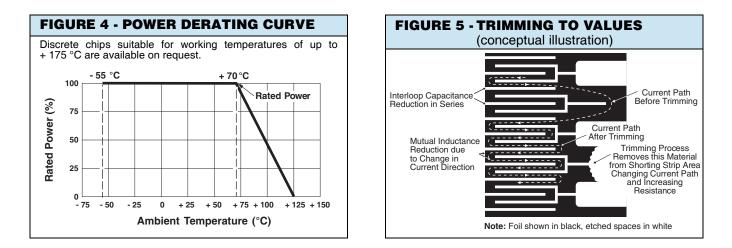
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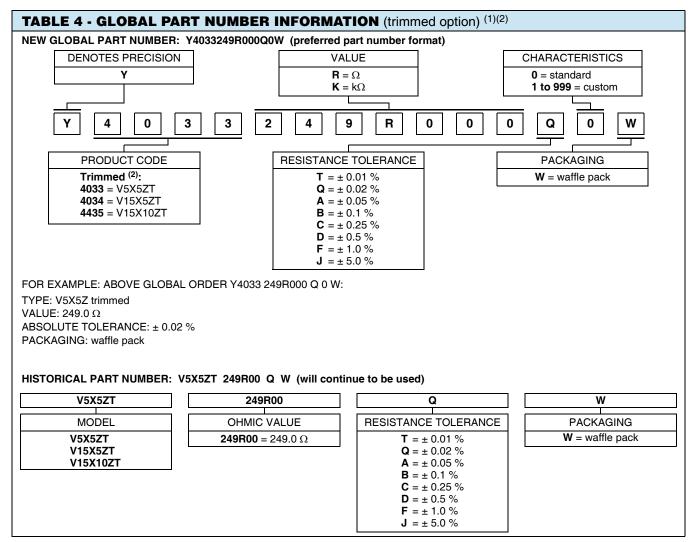
- ⁽¹⁾ TCR tracking is a measure of the similarity of resistance value change in two or more resistors which are undergoing the same temperature changes. Tracking could be expressed as the difference in the temperature coefficients of the resistors, expressed in ppm/°C as $(\Delta R_1/R_1 \Delta R_2/R_2) \times 10^{-6}/\Delta T$ °C.
- (2) Selected TCR tracking is available for specially ordered lots of resistors. The selected TCR tracking can be 3, 2, 1 and as close as 0.5 ppm/°C throughout the full temperature range. Should close TCR tracking be required for differing resistance values, contact the factory.

V5X5Z, V15X5Z, V15X10Z (Z-Foil)

Vishay Foil Resistors



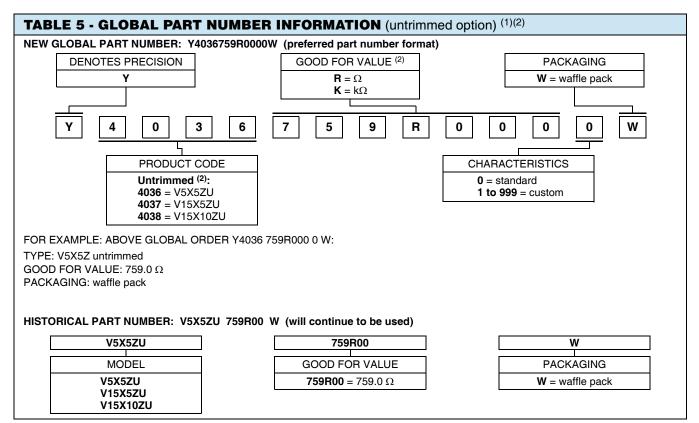




Notes

- ⁽¹⁾ For non-standard requests, please contact application engineering.
- ⁽²⁾ VISHAY to supply chips trimmed to the purchaser's exact resistance and tolerance specifications, ready for insertion into a hybrid microcircuit with no further processing other than bonding and termination. Specify exact resistance value(s) and tolerance(s).





Notes

⁽¹⁾ For non-standard requests, please contact application engineering.

⁽²⁾ To order user trimmable chips specify the final resistance value desired.



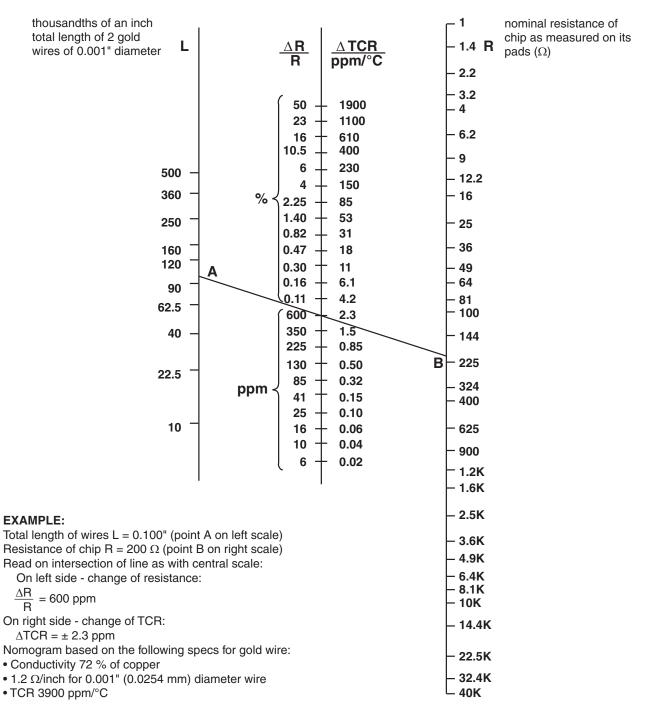
EFFECTS OF GOLD WIRE

The bonding of the gold wires to the chip has an effect on the overall resistance and on the temperature coefficient, according to the length of wire used.

The nomogram below shows the effect on both parameters with varying lengths of 0.001" (0.0254 mm) diameter gold wire.

NOMOGRAM

Change of resistance and TCR due to a length L of gold wire added at wire bonding.





Vishay Precision Group

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